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APPLICANT
Leonardi SalvatoreFILING DATE
December 28, 2001GROUP ART UNIT
2823**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
SK	AA	5,442,223	8/15/95	Fujii	257	506	
SK	AB	5,607,875	3/4/97	Nishizawa et al.	437	63	
SK	AC	5,914,523	6/22/99	Bashir et al.	257	520	
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	AI						
	AJ						

J1050 U.S. PTO
10/033694
12/28/01

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
SK	AK	0451454	10/16/91	EP		
	AL					
	AM					
	AN					
	AO					

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

SK	AP	R. Jerome et al., "The Effect of Trench Processing Conditions on Complementary Bipolar Analog Devices with SOI/Trench Isolation," <i>IEEE 1993 Bipolar Circuits and Technology Meeting 3.2</i> , :pages 41-44, 1993.
SK	AQ	Makoto Yoshida et al., "A Bipolar-Based 0.5 μ m BiCMOS Technology on Bonded SOI for High-Speed LSIs," <i>IEICE Transaction on Electronics</i> , E77-C(8):1395-1402, 1994.
	AR	

EXAMINER

DATE CONSIDERED 1/26/03

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.